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**INSTITUTE OF ENGINEERING
AND TECHNOLOGY FOR WOMEN**
(Affiliated to Visvesvaraya Technological University, Belagavi, Approved by AICTE New Delhi & Govt of Karnataka)
KRS Road, Metagalli, Mysuru - 570016



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

PRESS REPORT

Five-Day Workshop on “Certificate Course in Mastering Microcontroller Peripherals” for IV Semester Students

The Department of Electronics and Communication Engineering successfully organized a **five-day workshop** on “Certificate Course in Mastering Microcontroller Peripherals” for the IV Semester A, B, and C section students from **14th June 2024 to 29th June 2024**.



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Elevation: 791.5m
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Time: 24-06-2024 09:32
Note: A312 workshop



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Note: A312 workshop

Five-Day Workshop on “Certificate Course in Mastering Microcontroller Peripherals”

The workshop was conducted by distinguished industry experts, **Mr. Yashwanth B. R. H. R., PravegaSemi Pvt. Ltd.**, and **Mr. Vybhav M. N., FPGA Design Engineer, MaxLinear Technologies**, who shared their expertise and practical insights into the field of embedded systems and microcontroller applications.

The primary objective of the certificate course was to provide students with a strong foundation in microcontroller architecture and peripherals while equipping them with industry-relevant skills required for embedded system design and development. The program aimed to bridge the gap between theoretical concepts and practical implementation through hands-on training sessions.

Principal
Principal
GSSSIETW, Mysuru-16



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Press Report - 3 Days Workshop on “Device Driver Development with Protocol”



Interactive Hands on session during 3 days workshop on Device Driver Development with Protocol.



(From left) Mr.Yashwanth B, R, Mr. Vinod H R, Embedded Engineers PravegaaSemi private ltd, Mysuru. Dr. Rajendra R Patil, Professor and Head Department of ECE, GSSSIETW, Mysuru.



Mr. Yashwanth B, R, Embedded Engineer, PravegaaSemi private ltd, Mysuru, delivering lecture on Arduino Device Driver Development Protocol

Department of Electronics and Communication Engineering of GSSSIETW, Mysuru had organized 3 days workshop on “**Device Driver Development with Protocol**” for 3rd semester students on 4th, 5th, 7th(for A-Section), 11th, 12th, 13th(for B-Section) and 18th, 19th, 20th(for C-section) January 2024. A total of 9 sessions were carried out during the period. The resource person for the event was **Mr. Vinod H R and Mr. Yashwanth B R**, Embedded Engineers, PravegaaSemi private Ltd., Mysuru.

The workshop aimed to provide the fundamentals of Arduino programming, providing a comprehensive learning experience through a combination of theoretical sessions and hands-on activities.

The workshop began with a warm welcome and an overview of the agenda for the next three days. The first day focused on introducing participants to the basics of Arduino, such as understanding the history and significance of Arduino in the field of electronics and programming. Introduction to bare metal coding was provided followed by hands on with Blinking aLED and different sensor such as IR,LDR using bare metal coding.Practical demonstrations and exercises on connecting sensors to Arduino boards were thoroughly explained.

The second day of the workshop was completely based on basic protocols of Arduino i.e standard UART (Universal Asynchronous Receiver -Transmitter), SPI (Serial Peripheral Interface), and I2C (Inter-Integrated Circuit).Students gained knowledge on the basics of how different communication protocol works, how the data is stored in the backend application, advantage and limitations of those protocols.

The final day of the workshop was on the concepts of debugging & miniproject on combination of all the sensors and UART. Participants presented their mini projects, incorporating the skills and knowledge gained over the three days.

The three-day Arduino workshop proved to be a success, providing participants with a solid foundation in Arduino programming and hands-on experience in building electronic projects.

Participants expressed their satisfaction with the interactive sessions, and the opportunity to apply their learning in practical projects.

Finally, we would like to thank our Management, Principal and HOD for providing an opportunity to organize such a wonderful and informative workshop with hands on session for the student and helping them to gain their knowledge in the field of Embedded system.


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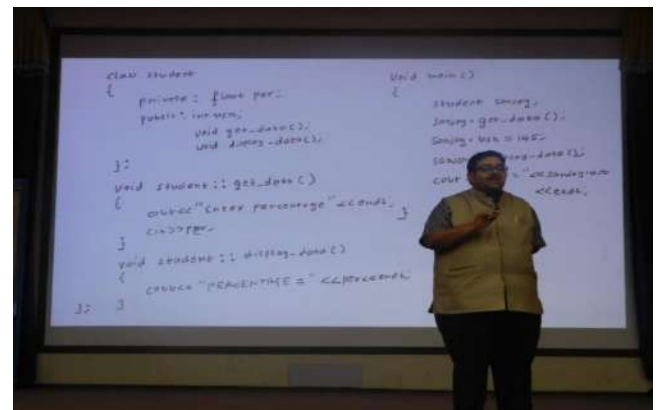


DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

PRESS REPORT

Introductory Session of 3-Day Training Program on “Embedded C & C++ and Industry Tool for Embedded Design”

The Department of Electronics and Communication Engineering organized the introductory session of a **three-day training program on “Embedded C & C++ and Industry Tool for Embedded Design”** for V Semester students with the objective of enhancing their technical competencies and bridging the gap between academic learning and industry requirements.



Introductory Session of 3-Day Training Program on “Embedded C & C++ and Industry Tool for Embedded Design”

The training program was designed to provide students with a comprehensive understanding of embedded systems programming, industry-standard development tools, and real-time applications in embedded design. The initiative aimed to equip participants with the practical skills required to meet the evolving demands of the embedded systems and semiconductor industries.

The inaugural session commenced with a warm welcome to the participants, followed by an overview of the training schedule and expected learning outcomes. The resource person highlighted the growing significance of embedded technologies across various sectors, including consumer electronics, automotive systems, industrial automation, healthcare devices, and the Internet of Things (IoT).

During the introductory session, students were introduced to the fundamentals of Embedded C and C++ programming, microcontroller-based system design, debugging techniques, and the use of industry tools employed in embedded software development. The session also emphasized the importance of hands-on learning, problem-solving skills, and industry-oriented practices.

The resource person provided valuable insights into current trends, career opportunities, and the essential technical skills required to excel in the field of embedded systems. Students actively participated in the interactive discussions and expressed enthusiasm about gaining practical exposure through the forthcoming hands-on sessions.

The three-day program is expected to strengthen students' programming abilities, enhance their understanding of embedded design workflows, and prepare them for internships, projects, and career opportunities in the embedded systems domain.

The Department expresses its sincere gratitude to the Management, Principal, and Head of the Department for their constant encouragement, valuable guidance, and unwavering support in organizing this training program successfully.

The Department also extends its heartfelt appreciation to the resource person for sharing expertise and providing valuable industry insights to the students.

R. K. Ravi
HOD, ECE

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Dr. S. S. Srinivas

Principal
Principal
GSSSIETW, Mysuru-16

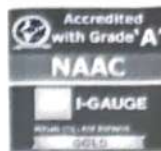


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SRI M. GOVINDA RAO

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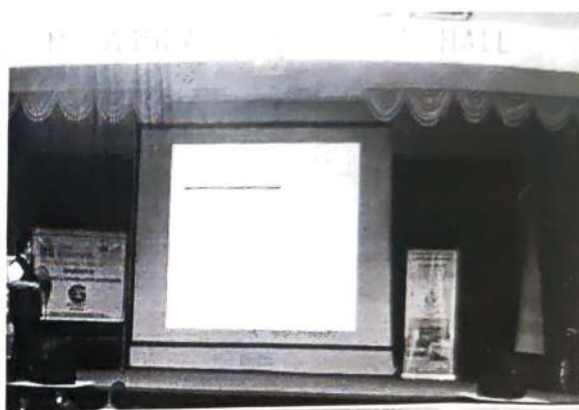


DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

PRESS NOTE

Date: 01/01/2024

**Two days Training Program on
“Competitive Programming /skills using C & C++” held at GSSSIETW**



Glimpses of the two days training program on “Competitive Programming /skills using C & C++”

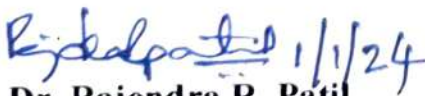
Department of Electronics and Communication Engineering in association with IETE Students Forum (ISF) organized a two days training program on **“Competitive Programming skills using C & C++”** on 28th and 29th Dec 2023 for the 7th Sem Students at Sri. M Govinda Rao Memorial Hall. The Resource person was Dr. Ramprasad H C, Head-Academics & Operations, 10 Seconds, Bengaluru.

Dr. Rajendra R. Patil, Professor & Head, ECE Dept. welcomed the students for the event and emphasised on the importance of the training program for their career.

The objective of the training was to up skill the students with logic building skills and to prepare them for placements. The resource person gave the students an insight of how an interview process is conducted and what the interviewers expect from the candidates while hiring. The technical session started with delivery of the basic concepts in C programming. Different approaches that should be followed while solving a programming challenge were also discussed. Next programming concepts such as the declaration of variables, asymptotic notations, time complexity and space complexity were explained with examples.

Later during the course of training, the resource person gave an overview of C++ programming specifically the object oriented programming concepts. Class and object declaration, and the usage of access specifiers with proper analogies were demonstrated. Pointers and overriding concepts were also discussed. This training provided the students with an edge in Programming skills from IT placement perspective.

Smt. Vanaja B Pandit, Hon. Secretary, GSSS (R), Sri. R K Bharath CEO & MCM, GSSS (R), Smt. Anupama B. Pandit, Administrative Officer, GSSS (R), and Dr. Shivakumar M, Principal, GSSSIETW had conveyed their best wishes for the success of the event.


Dr. Rajendra R. Patil
HoD, ECE
Head of the Department
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GSSSIETW, Mysuru, India-570016


Dr. M Shivakumar 01/01/2024
Principal



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 22-12-2023

Press Note

Online Technical Webinar on “Trends and Challenges in VLSI Fabrication Technology” held at GSSSIETW, Mysuru



Glimpse of the Online Technical Webinar

The Department of Electronic and Communication Engineering, GSSS Institute of Engineering and Technology for Women, Mysuru, organized an Online Technical Webinar on “**Trends and Challenges in VLSI Fabrication Technology**” on 22/12/2023 for 7th Semester ECE students. **Mr. Samuel Tensingh**, Associate Lecturer at the School of Biomedical Engineering at the University of Sydney, Australia was the Resource Person.

The session was commenced by welcoming the resource person and the participants. **Dr. Rajendra R. Patil**, Professor & Head, Dept. of ECE briefed the participants about the significance of VLSI technology and motivated all the participants to make the best use of this event as it helps them to enhance knowledge about opportunities to build their skills in the VLSI core domain.

In session the resource person gave presentation about the comprehensive overview of the current landscape of semiconductor manufacturing and this event aimed to address the advancements and hurdles in VLSI fabrication, offering valuable insights for professionals in the field.

Resource person started the session with briefing digital world, Evolution of Semiconductors which captures the dynamic and influential role that semiconductors play in shaping the modern technological landscape, about VLSI circuits and its applications in different areas and detailed explanation of IC design flow by presenting the video.

Mr. Samuel Tensing also presented CMOS Fabrication Sequence and Advanced CMOS process provides a comprehensive understanding of the state-of-the-art technologies shaping the semiconductor industry and also Design rules offering a glimpse into the pivotal aspects of design rules in the semiconductor design and fabrication. Finally, he concluded with general information about VLSI certification and job roles and opportunities for the participants.

As a whole, session was very interactive and it would help participants to grab an opportunity in the core VLSI field. Resource person clarified all the doubts and questions asked by the participants. Over 187 students of 7th Semester ECE participated in the event. This event was appreciated by all the participants.

Smt. Vanaja B Pandit, Hon. Secretary, GSSS (R), Sri. R K Bharath CEO and MCM, GSSS (R), Smt. Anupama B. Pandit, Administrative Officer, GSSS (R) and Dr. M. Shivakumar, Principal, GSSSIETW, had conveyed their best wishes for the successful of the event.

Rajendra R. Patil 27/12/23
Dr. Rajendra R. Patil
HoD, ECE

Head of the Department
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M. Shivakumar 27/12/2023
Dr. M Shivakumar
Principal
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

PRESS REPORT

Date: 13.12.2023

One day Workshop on “Introductory Session on VLSI Technology” held at GSSSIETW, Mysuru



Figure : (From left) Dr. M Shivakumar, Principal, GSSSIETW, Mysuru, Mr. Sachin Kumar Patil, Career Coach, Technology trainer, Business, innovation, and Technology Consultant, and Mr. Pradeep Kumar Chikkamath, Principal Consultant, Learning Links Foundation and Dr. Rajendra R Patil, Prof & Head, Dept. of ECE, GSSSIETW, Mysuru

The Department of Electronics & Communication Engineering, GSSS Institute of Engineering & Technology for Women, Mysuru organized one day workshop on “**Introductory Session on VLSI Technology**” in association with learning links foundation on 8th December 2023 for III semester students.

Dr. Rajendra R Patil, Professor and Head, Dept. of ECE, GSSSIETW, Mysuru gave a briefed about “**Intel India STEM Skills for Women 2023 - 2024**” which is a program implemented by Learning Links Foundation and supported by Intel India to the gathering. This program specifically focuses on imparting training and capacity development on VLSI technology for female students pursuing their B. E, B.Tech / M. Tech degrees from colleges across Karnataka,

Andhra Pradesh & Telangana. Apart from technical training, students will also be enabled on soft skills like communication, teamwork, time management, etc. to make them job-ready.

In his address he gave the demand of Very Large-Scale Integration (VLSI) technology. He mentioned the growth of VLSI and the number of job opportunities to grow in VLSI design area making it a lucrative career option for many.

Dr. Shivakumar M, Principal, GSSSIETW, Mysuru highlighted the importance of the current Technology era that require the skill towards the progress of any country and how each country gives importance to engineers.

The resource persons of the day were an intellectual duo- Mr. Sachin Kumar Patil, Career Coach, Technology trainer, Business, innovation, and Technology Consultant, and Mr. Pradeep Kumar Chikkamath, Principal Consultant, Learning Links Foundation. They educated the future women engineers on the booming VLSI technology. The students were very interested to learn and know more about the evolution of the VLSI technology from the first gigantic 5MB hard disc by IBM in 1950 to today's minute 4TB ICs.

The session was extremely beneficial for the students to know the importance of VLSI, its applications, and future job opportunities in the core domain of ECE and EEE. The resource persons encouraged the students by illustrating the pros of STEM, thereby encouraging them to choose the same domain for their work in the future.

The session highlighted the importance of the Semiconductor industry and its boom in India. Hence, they told the students what kind of skills they needed to develop to step into the electronic industry. Resource person also offered the students a free course on VLSI that can help them learn in a hybrid way.

This session gave the students answers to questions like What, How, and Why VLSI? At the end of the session, the students were energized, interested, and motivated to dive deep into the field of VLSI. The experts truly managed to articulate Very Well on Very Large-Scale Integration.

The program was extremely informative and a good kick start for all the tech minds at GSSSIETW.

Smt. Vanaja B Pandit, Hon. Secretary, GSSS (R), Sri. R K Bharath CEO & MCM, GSSS (R), and Smt. Anupama B. Pandit, Administrative Officer, GSSS (R) had conveyed their best wishes for the successful of the event.



Press Report Prepared by
Anitha K

Assistant Professor, Dept of ECE
GSSSIETW, Mysuru



Dr. Rajendra R Patil
HoD, ECE
GSSSIETW, Mysuru

Head of the Department
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Press Note

Date: 16/11/2023

**Two Days Technical Talk for 5th Semester ECE Students held at
GSSSIETW, Mysuru.**



Glimpses of 2 Days technical talk

Department of Electronics and Communication Engineering permanently affiliated to VTU Belagavi and Accredited by NBA, New Delhi, GSSS Institute of Engineering and Technology for Women, Mysuru under AECES and IQAC organized a series of insightful technical talks for 5th semester ECE students on 16th and 17th November 2023. The technical talks aimed at unravelling the mysteries of future in VLSI (Very Large Scale Integration) technology, Internet of Things (IoT), and success stories that showcase the relentless pursuit of innovation as a part of internship for 5th semester ECE students.

Day one started with a technical talk on “**The Future of VLSI Technology**” by **Mr. Srinivasa B A**, Senior Delivery Manager, L&T Technology Services Ltd, Mysuru. Mr. Srinivasa B A discussed the cutting-edge developments in shaping the future of microelectronics, from the latest advancements in chip design to breakthroughs in fabrication processes. Students gained a

participants. Over 160 students of 5th Semester ECE students participated in the event. This event was appreciated by all the participants.

profound understanding of the possibilities and challenges that lie ahead in the dynamic VLSI field.

The session was continued by **Mr. Ravishankar G Shiroor**, COO-Technology and Digital Access-network Stellapps, Bangalore. Mr. Ravishankar G Shiroor captivated students about IoT and Success story of Stellapps. In his deliverables, he conveyed that, as the world becomes increasingly interconnected, the Internet of Things is emerging as a transformative force. Mr. Ravishankar delved into the intricacies of IoT, exploring its applications across industries and discussed the potential it holds for shaping a smarter, more connected world. He also shared the remarkable success story of Stellapps, a trailblazer in dairy technology and told how Stellapps has harnessed innovation to transform the dairy industry, leveraging technologies such as IoT and data analytics to enhance efficiency and sustainability.

In the afternoon session, Mr. Kiran Marathe, CEO and founder, Deshila Technology Research Institute, Mysuru shared the success saga of a startup at the forefront of 5G communication. Mr. Kiran Marathe explained how his visionary company has disrupted the telecommunications landscape, pushing the boundaries of connectivity and setting new standards for speed and reliability.

Afternoon session was concluded by **Mr. G S Sumogh**, Managing Director, Eagle Eye Drones Pvt. Ltd, Mysuru. Mr. G S Sumogh educated students about Drones and their difference with aircraft. During his session, participants witnessed the transformative power of drone technology through the success story of Eagle Eye Drones from surveillance to logistics. This session highlighted the diverse applications and successes of drones in various industries.

Day two sessions was initiated by **Mr. Ashik**, Managing Director, Panjurli Labs, Pvt. Ltd., Mysuru on Climate Tech and Green Tech start up. Mr. Ashik, emphasized the need for innovative solutions to address environmental challenges. The speaker also highlighted the role of technology in mitigating the impact of climate change and fostering sustainability. Mr. Ashik discussed the challenges faced and lessons learned, and offered valuable insights for aspiring entrepreneurs in the audience.

The event featured an engaging Q&A session where attendees had the opportunity to pose questions to resource persons. The interactive dialogue touched upon various aspects of VLSI, IoT and entrepreneurship. Attendees had the opportunity to seek clarification and gain further insights into the practical aspects of implementing tech solutions for real-time challenges.

As a whole, sessions were very interactive, informative and would help students to go a long way in educational journey. Resource Persons clarified all the doubts and questions asked by

participants. Over 160 students of ^{5th} Semester ECE students participated in the event. This event was appreciated by all the participants.


Dr. Rajendra R Patil
HoD, ECE


Dr. Shivakumar M
Principal 20/11/2023

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

PRESS REPORT

Workshop on “Problem Solving Techniques Training”

The Department of Electronics and Communication Engineering successfully organized a one-day workshop on “**Problem Solving Techniques Training**” for V Semester students on **31 October 2023**.

The session was conducted by **Dr. Ramprasad H. C., Head – Academics & Operations, 10 Seconds, Bengaluru**, an experienced academician and industry expert in the field of problem-solving methodologies and skill development.



One-day workshop on “**Problem Solving Techniques Training**” for V Semester students on **31 October 2023**.

The primary objective of the workshop was to equip students with effective problem-solving strategies, analytical thinking skills, and structured approaches to tackle complex engineering problems. The program focused on enhancing students' logical reasoning abilities, algorithmic thinking, and aptitude for competitive examinations and placement opportunities.

During the interactive session, Dr. Ramprasad introduced various problem-solving frameworks, emphasizing the importance of understanding problem statements, identifying constraints,

developing optimal solutions, and validating outcomes systematically. The workshop covered topics such as decomposition of complex problems, critical thinking techniques, time management during problem-solving and practical approaches to coding and aptitude challenges.

Through real-time examples, case studies, and hands-on exercises, participants gained valuable insights into applying problem-solving techniques in academic projects, technical interviews, and industry-oriented scenarios. Students actively engaged in discussions and collaborative activities, which fostered teamwork and improved their decision-making capabilities.

The workshop significantly contributed to strengthening the students' technical competencies and preparing them for future career opportunities. Participants appreciated the practical orientation of the training and the expert guidance provided during the session.

The event concluded with an interactive question-and-answer session, followed by a vote of thanks to the resource person for sharing his expertise and valuable insights. The workshop proved to be highly beneficial in enhancing the problem-solving mindset and professional readiness of the students.

The department remains committed to organizing such industry-oriented training programs to bridge the gap between academic learning and professional expectations.

The Department expresses its sincere gratitude to the Management, Principal, and Head of the Department for their constant encouragement, valuable guidance, and unwavering support in organizing this workshop successfully. Their commitment to promoting industry-oriented learning experiences and skill development initiatives continues to inspire students to achieve academic excellence and professional competence.

The Department also extends its heartfelt appreciation to the resource person, Dr. Ramprasad H. C., Head – Academics & Operations, 10 Seconds, Bengaluru, for sharing his expertise and enriching the participants with valuable insights into effective problem-solving techniques.


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Department of Electronics & Communication Engineering

Press note

Date: 02-05-2023

Graduate Aptitude Test in Engineering (GATE) Awareness Program held at GSSSIETW, Mysuru on 13/04/2023.



Glimpses of GATE Awareness Program

Department of Electronics and Communication Engineering under AECES forum, GSSS Institute of Engineering and Technology for Women, Mysuru, Accredited by NBA, New Delhi, organized "Gate Awareness Program" for 6th semester ECE students, under IQAC on Thursday 13/04/2023.

"Dr. Raghunath, RLC Solutions, Bangalore was the resource person for the technical talk. The session was commenced by welcoming the panel members, resource persons and participants.

Dr. Rajendra R. Patil, Professor & Head, Dept. of ECE briefed the participants about significance of Gate Awareness Program. He motivated all the participants to make best use of this event as it helps them to enhance knowledge about higher studies after degree.

The objective of the **Gate Awareness Program** was to create awareness among students, about the importance of GATE and its benefits. Dr. Raghunath gave information to students about the eligibility criteria, exam pattern, syllabus, and registration process. He also emphasized on the tips and strategies for effective preparation, guidance on selecting the right books, study materials, and time management.

Dr. Raghunath encouraged the students to pursue higher education, which can lead to better career opportunities and a stronger workforce. He explained the importance of understanding the ECE subjects such as Electromagnetic waves, Signals and Systems, Digital Signal Processing, Network analysis.

As a whole this session was very interactive, informative and would help students go a long way in educational journey. Resource Persons clarified all the doubts and questions asked by participants. Over 160 students of 6th Semester ECE students participated in the event. This event was appreciated by all the participants.



Prepared by

Asha M



HOD, ECE

Head of the Department
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Principal