



**GSSS**

GEETHA SHISHU  
SHIKSHANA SANGHA(R)

**INSTITUTE OF ENGINEERING  
AND TECHNOLOGY FOR WOMEN**

(Affiliated to Visvesvaraya Technological University, Belagavi. Approved by AICTE New Delhi & Govt. of Karnataka )

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION**

Date: 18-02-2025

**Report for  
Two days Faculty Development Program on "Demonstration and  
Utilization of Vector Network Analyzer (VNA) for Antenna  
Applications" held at GSSSIETW, Mysuru**



**Glimpses of "Demonstration and Utilization of VNA for Antenna Applications" held during 12<sup>th</sup> & 13<sup>th</sup> Feb 2025.**

The Department of Electronics & Communication Engineering at GSSSIETW, Mysuru, successfully organized a two-day Faculty Development Program (FDP) on "Demonstration and Utilization of Vector Network Analyzer (VNA) for Antenna Applications" on 12th and 13th February 2025. The resource person was Mr. Raja, Manager - Product Development, Enthi Tech Solutions Pvt Ltd., Coimbatore. The event aimed to enhance faculty knowledge and expertise in Vector Network Analyzers, specifically the SVA1075X, to analyze and optimize antenna performance in research and practical applications.

Around 20 faculty members from the ECE department registered and attended the program. The sessions provided hands-on training and theoretical insights into the working principles, calibration techniques, and practical applications of the SVA1075X Vector Network Analyzer.

The SVA1075X is a state-of-the-art Vector Network Analyzer that offers precise measurements for characterizing antennas, filters, and RF components. It provides high accuracy in measuring S-parameters, reflection coefficients, impedance matching and a wide frequency range for diverse antenna and RF applications. It also offers a user-friendly interface with advanced features for signal analysis and network characterization. The training focused on real-world applications of VNA in antenna design, impedance matching, and wireless communication research, making it a valuable tool for faculty involved in RF and microwave engineering.

The FDP included expert sessions on theoretical and practical aspects of VNA, hands-on demonstrations for real-time antenna performance evaluation, and interactive Q & A sessions to clarify doubts and explore advanced applications. Participants were appreciative of the informative sessions and hands-on experience of SVA1075X VNA.

Dr. Rajendra R. Patil, Professor and HoD, Department of Electronics & Communication Engineering, highlighted the importance of the SVA1075X Vector Network Analyzer. Furthermore, the HoD congratulated all the faculty members of the ECE department for their successful participation in the two-day FDP. The FDP concluded with a vote of thanks, acknowledging the efforts of the organizers, resource persons, and participants in making the program a success. Additionally, all registered participants received e-certificates upon successful completion of the program.

The event was highly beneficial in equipping faculty members with practical skills and knowledge to enhance their research and teaching methodologies in antenna applications.



HoD, ECE 18/02/25

Head of the Department  
Dept of Electronics & Communication  
GSSSIETW, Mysuru, India-5700



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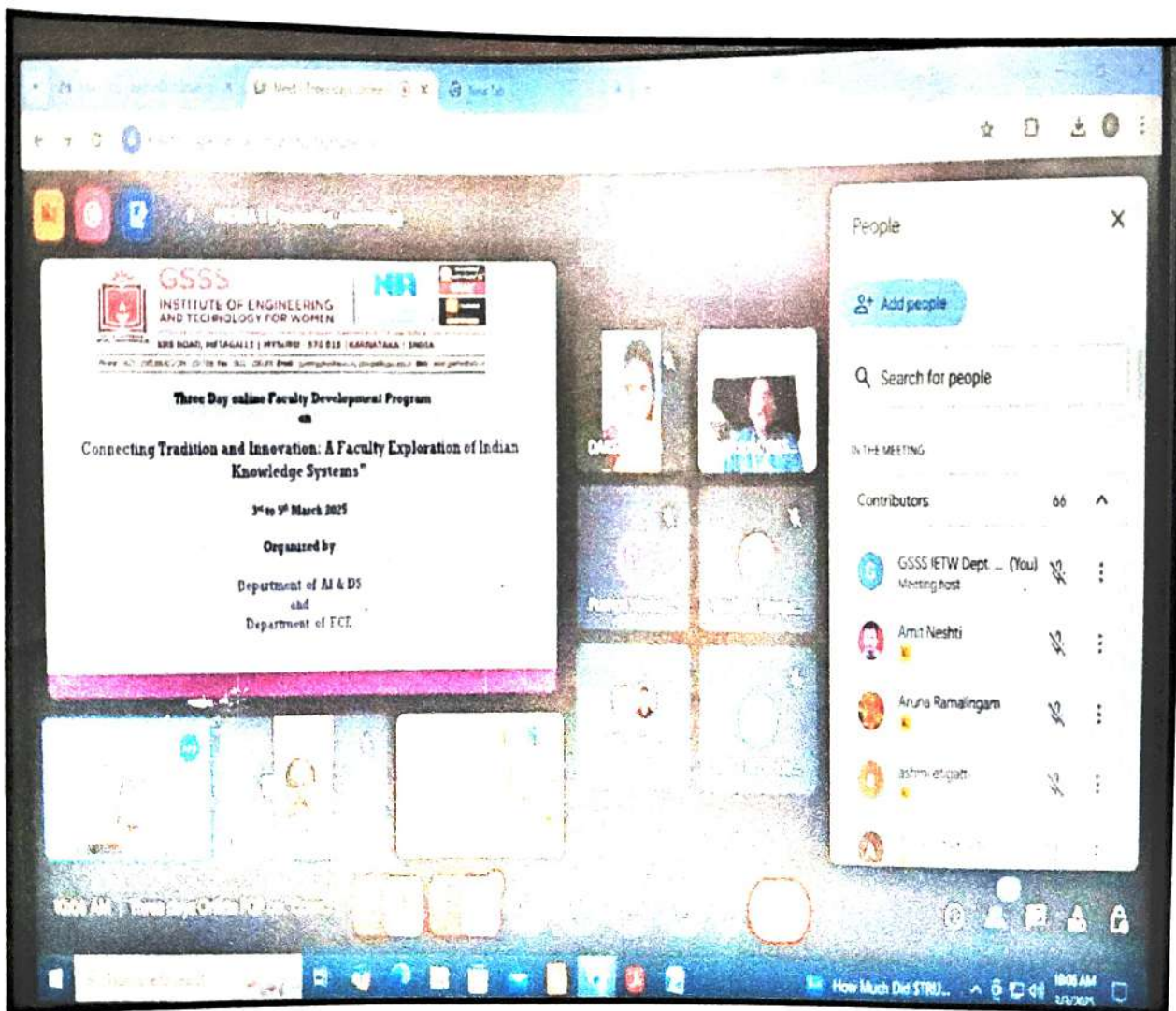


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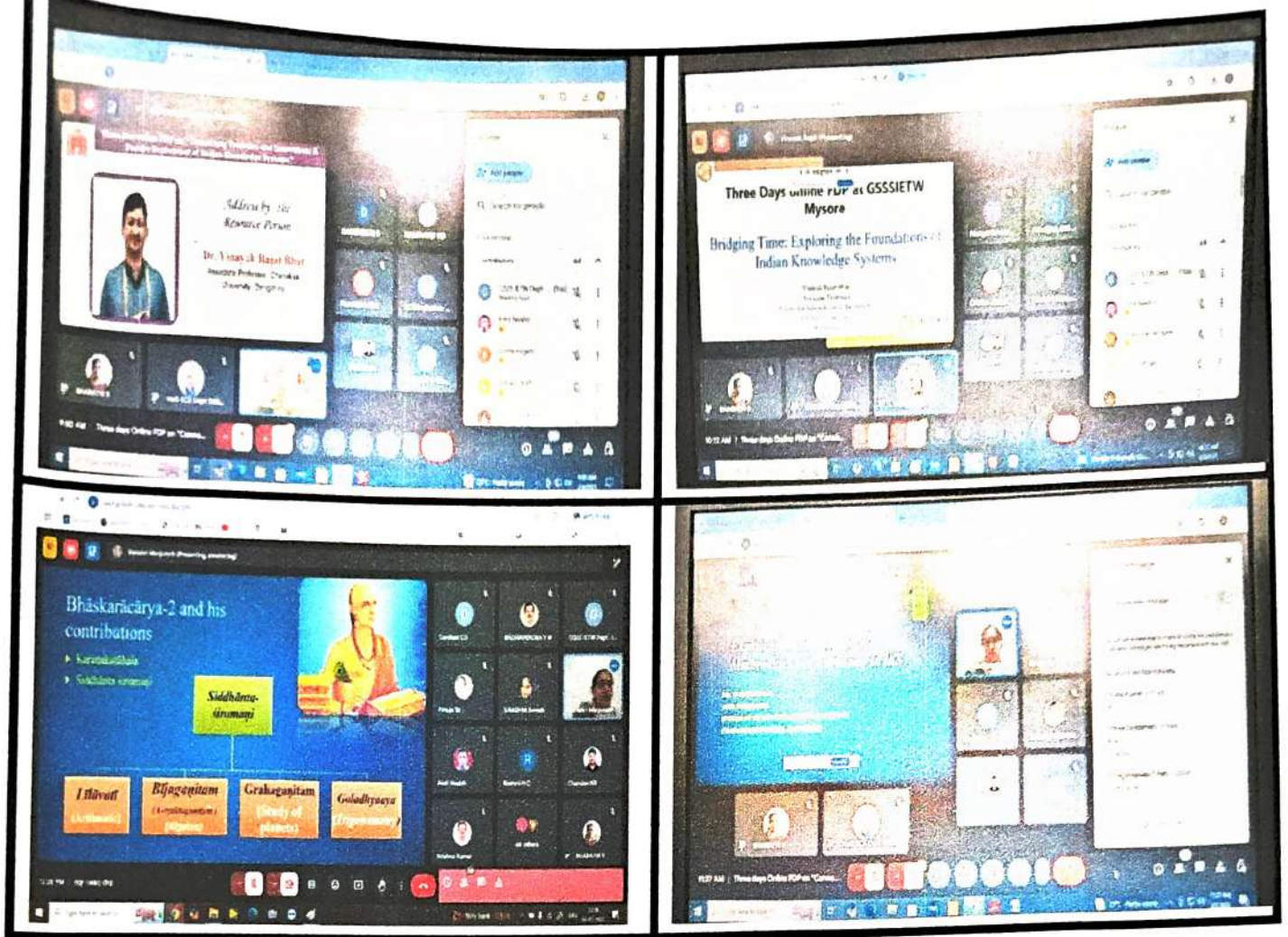
### DEPARTMENT OF ELECTRONICS AND COMMUNICATION

## Press Note

# Three days Online FDP & "Connecting Tradition and Innovation: A Faculty Exploration of Indian Knowledge Systems"



Glimpse of online FDP inauguration and participants



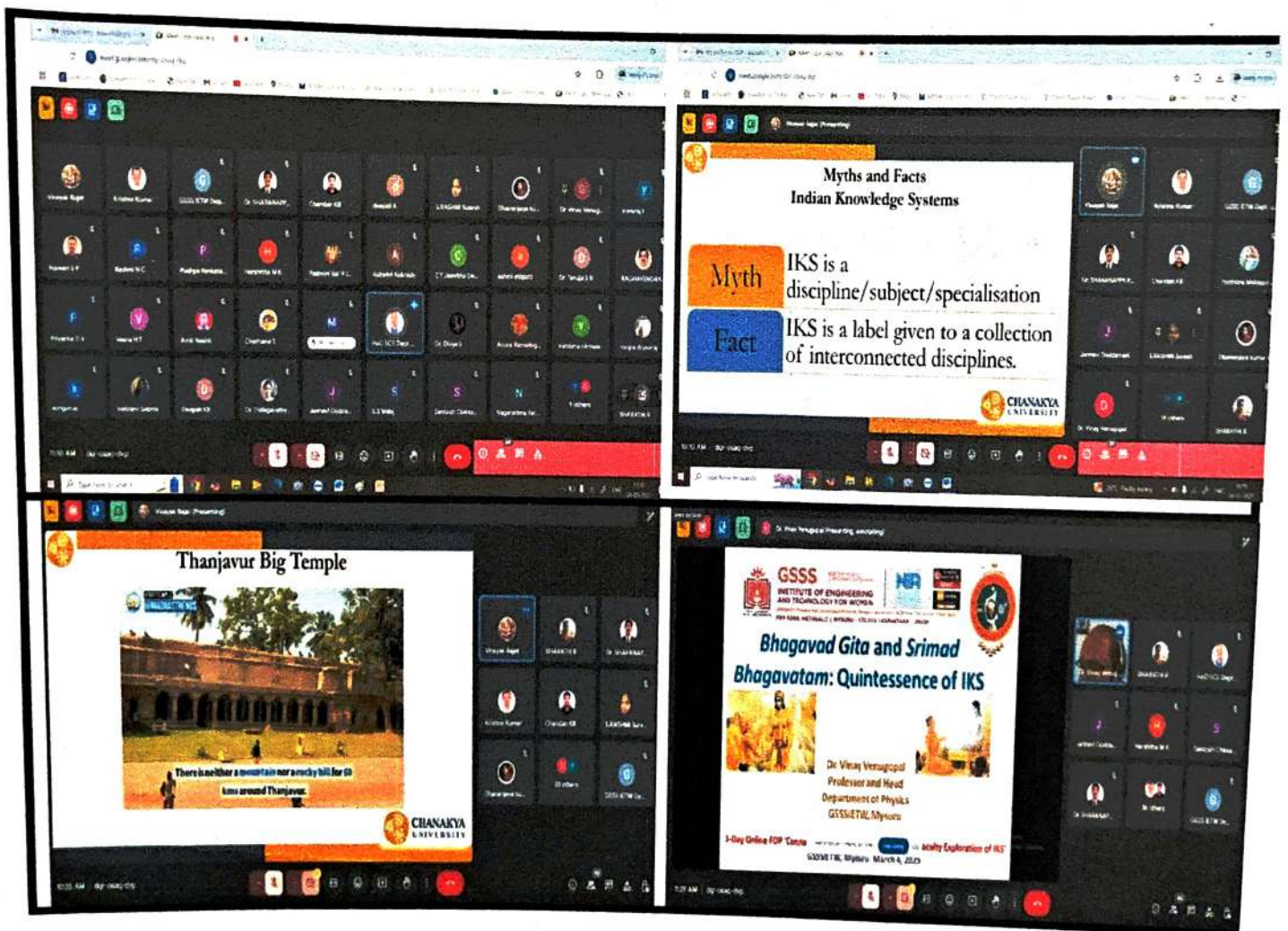
### Glimpse of Day One Sessions

Department of Electronics and Communication Engineering, GSSS Institute of Engineering and Technology for Women, Mysuru, permanently affiliated to VTU Belagavi and Accredited by NBA, New Delhi, and Department of Artificial Intelligence and Data Science, GSSS Institute of Engineering and Technology for Women (GSSSIETW), Mysuru, jointly organized a three-day Faculty Development Program (FDP) on **“Connecting Tradition and Innovation: A Faculty Exploration of Indian Knowledge Systems”** from 3<sup>rd</sup> to 5<sup>th</sup> March 2025.

Event inauguration begin with an invocation by Ms. Chaithashree S R, Assistant Professor, Department of EEE, followed by a welcome address by Mrs. Harshitha M K, Assistant Professor, Department of ISE. Dr. Rajendra R. Patil, Professor & Head, Department of ECE, provided an overview of the FDP, detailing the session structure and the participant assessments conducted at the end of each day. He also highlighted the overwhelming response from various institutes and universities across the state. A total of 65 participants from institutions in Anekal, Bagalkote, Bengaluru, Belagavi, Bidadi, Davangere, Hubballi, Kalaburagi, Laxmeshwar, Mangaluru, Mudhol, Mysuru, Nidasoshi, Ponnampet, Puttur, Shivamogga, and Ujire registered for the program.. Mrs. Darshini M S, Assistant Professor, Department of CSE, introduced the resource person. Dr. M. Shivakumar, Principal of GSSSIETW, commended the efforts of the organizing team and emphasized the significance of this multidisciplinary platform in bridging the gap between traditional knowledge and modern technology.

On Day1, the morning online session featured Dr. Vinayak Rajat Bhat, Associate Professor at Chanakya University, Bengaluru, who delivered an insightful lecture on “Bridging Time: Exploring the Foundations of Indian Knowledge Systems,” providing a comprehensive understanding of IKS principles. In his session, Dr. Bhat delivered the core principles of IKS, shedding light on its historical evolution, philosophical underpinnings, and its relevance in contemporary education. He emphasized the richness of India’s traditional knowledge, covering aspects such as ancient sciences, linguistic traditions, and cultural wisdom.

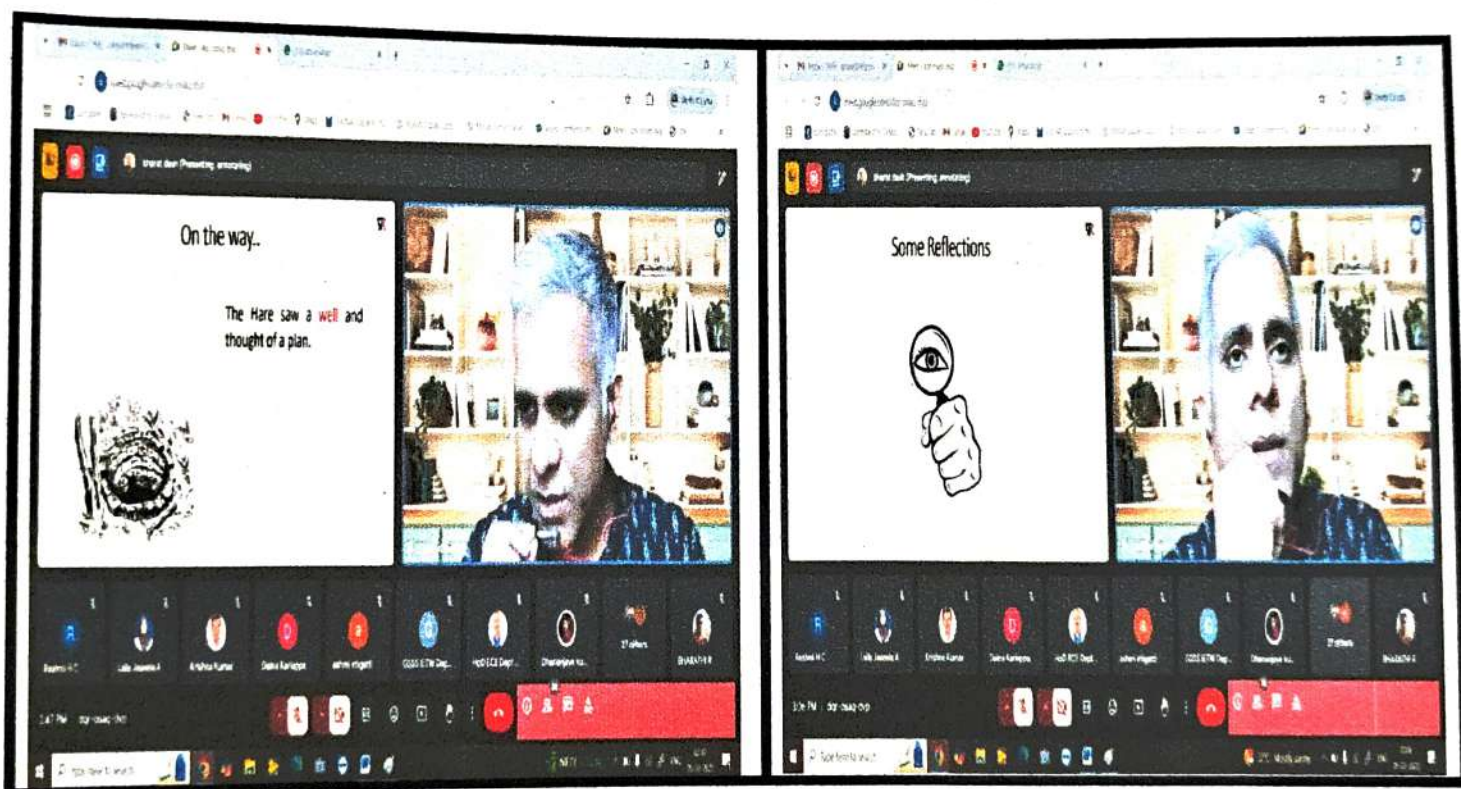
The afternoon session was conducted by Dr. Vanishri Bhat, Assistant Professor at Chanakya University, Bengaluru, who delivered an engaging and insightful lecture on “Exploring the Intersection of Mathematics and Astronomy in Indian Knowledge Systems (IKS).” She highlighted the profound mathematical and astronomical contributions of ancient Indian scholars, discussing concepts such as Vedic mathematics, the significance of zero, trigonometry, and complex astronomical calculations that predated modern discoveries. The session sparked curiosity and encouraged further exploration of how traditional Indian knowledge can complement contemporary scientific advancements.



Glimpse of Day two Sessions

On Day 2, the morning online session featured Dr. Vinayak Rajat Bhat, delivered an enlightening talk on “Decoding Indian Knowledge Systems: Insights from Ancient Traditions.” His session provided a deep and comprehensive understanding of IKS, shedding light on its historical evolution, cultural significance, and its relevance in modern education and research. Following this, Dr. Vinay Venugopal, Professor and Head of the Department of Physics at GSSSIETW, Mysuru, conducted a session on the “Bhagavad Gita & Bhagavatam: Quintessence of IKS” .His lecture emphasized the profound philosophical teachings embedded in these scriptures and their practical relevance in contemporary life. He highlighted key concepts such as dharma (duty), karma (action), and self-realization, demonstrating how these ancient texts offer timeless wisdom applicable to personal growth, ethical decision-making, and leadership.

At the end of each day, the FDP concluded with an online assessment, enabling participants to actively engage with the material and demonstrate their understanding of the topics discussed. This interactive evaluation process reinforced learning and encouraged deeper reflection on the knowledge imparted throughout the sessions.



### Glimpse of Day Three Sessions

On the final day, participants showcased their learnings on Indian Knowledge Systems, with their presentations reviewed by Dr. Vinay Venugopal, Professor & Head, Department of Physics, GSSSIETW, Mysuru.

The concluding session featured Dr. Bharat Dash, Professor at Sri Sri University, Cuttack, Odisha, who delivered an insightful talk on "From Vedas to Governance: Indian Knowledge Systems in Public Administration," exploring the application of ancient wisdom in modern governance and policy-making. The primary objective of this FDP program was to enhance awareness of Indian Knowledge Systems (IKS) and their relevance in engineering and technology. The sessions offered valuable perspectives on how traditional wisdom can complement contemporary innovations, providing participants with a strong foundation for future learning and research.

The event was successfully coordinated by Ms. Bharathi R, Assistant Professor, Department of AI & DS, and Ms. Rashmi H C, Assistant Professor, Department of ECE, GSSSIETW, Mysuru, ensuring its smooth execution and impactful learning experience.

Smt. Anupama B. Pandit, Secretary, GSSS(R), and Sri. R. K. Bharath, Board Member & CEO, GSSS(R), appreciated the organizing departments for successfully conducting the event.



**Dr Roopashree H R**  
HoD, AI&DS



**Dr Rajendra R Patil**  
HoD, ECE



**Dr. Shivakumar M**  
Principal,  
GSSSIETW, Mysuru

10/03/2025



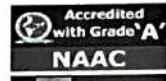
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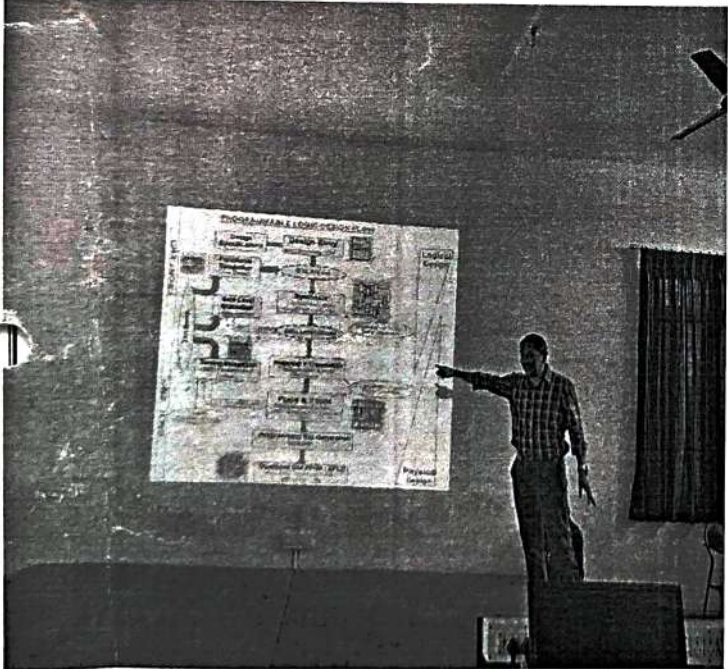


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**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**Press Report on**

**Two days FDP on "Artix7 FPGA 100k Development Board "held at  
GSSSIETW, Mysuru**



Mysuru, Karnataka, India  
GSSSIETW, ECE DEPARTMENT, A312, Mysuru, Karnataka 570003, India  
Lat 12.362202° Long 76.629038°  
04/03/2026 11:56 AM GMT +05:30  
Note : FDP'artix7 FPGA 100kboard Captured by GPS Map Camera

**Glimpse of Two days FDP on "Artix7 FPGA 100k Development Board "**

Department of Electronics and Communication Engineering (ECE) had organized two days FDP on "Artix7 FPGA 100k Development Board "on 4<sup>th</sup> & 5<sup>th</sup> March 2025 for faculties of ECE & EEE department of GSSSIETW Mysuru.

The Faculty Development Program (FDP) on Artix-7 FPGA 100k Development Board was organized with the aim of enhancing the knowledge and skills of faculty members in the field of digital circuit design, embedded systems, and FPGA (Field Programmable Gate Array) technologies. The FDP aimed to introduce the concepts of FPGA architecture, design techniques, and practical implementation using the Artix-7 FPGA platform, which is widely used in academic research and industry applications.

**Mr. Srikanth, Engineer, Advanced Electronics Systems-Bengaluru** was the Resource person for the FDP.

The FDP began with a warm welcome to the distinguished resource person, Principal Dr. Shivakumar M, Dr.S Padmashree, Professor, Dept of ECE, as well as the participates.

**Day 1:** Covered with the understanding the architecture of the Xilinx Artix-7 FPGA 100K, its advantages over traditional microcontrollers and ASICs, using Vivado Design Suite for synthesis, simulation, implementation, writing and debugging Verilog code for FPGA-based designs.

**Day 2:** Covered with the Implementation of Digital Circuits and Systems and exploring memory elements, finite state machines (FSM), and arithmetic circuits.

The FDP concluded with closing remarks from the coordinator, who thanked the speakers, faculties, and for their active participation.

Smt. Anupama B. Pandit, Secretary, GSSS (R), Sri. R K Bharath, CEO & MCM, GSSS (R), and Dr. Shivakumar M, Principal, GSSSIETW conveyed their best wishes for the success of the 2 days FDP.

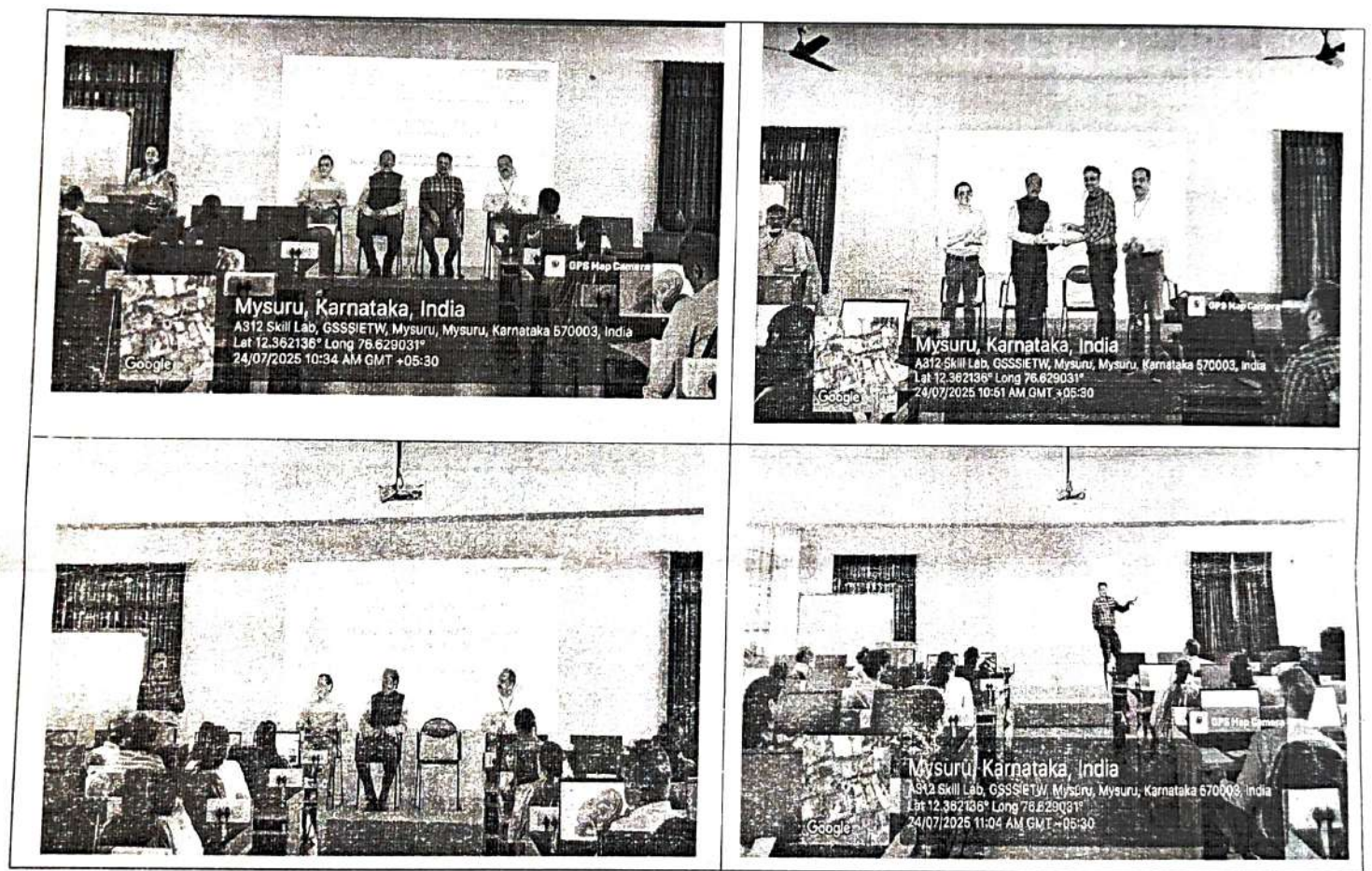
  
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Principal  
Principal  
GSSSIETW, Mysuru-16



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Press Report**  
**FDP on Embedded systems and Edge AI-Industry Perspective”**  
**On 24<sup>th</sup> July 2025**



**Glimpses of FDP held on 24<sup>th</sup> July 2025.**

The Department of Electronics & Communication Engineering at GSSS Institute of Engineering and Technology for Women (GSSSIETW) Mysuru has organized a Faculty Development Program (FDP) on “ **Embedded Systems and Edge AI- An Industry Perspective**” on **24<sup>th</sup> July 2025** under IETE students Forum. The event was conducted with an objective to provide faculty members with in-depth knowledge in Embedded systems design and Edge AI technologies, enabling them to understand hardware-software integration, development of intelligent edge applications, and incorporate emerging trends into teaching, research, and innovative projects.

The Faculty Development Programme (FDP) commenced with a formal inaugural session, marked by the presence of distinguished faculty and industry experts. The session began with a welcome address by **Dr. Latha M.**, Associate Professor, Department of ECE, who warmly greeted the dignitaries and participants.

This was followed by an address by **Dr. Rajendra R. Patil**, Professor and Head, Department of ECE, who provided valuable insights into the FDP's theme. He highlighted the current trends in Embedded Systems and Edge AI, discussing their growing relevance in industrial applications and the importance of integrating these advancements into academic curricula.

The Chief Guest and resource person, **Mr. Ashok Govindarajan**, Head of Solutions – Wireless Networks, Zilogic Systems, Chennai, delivered an insightful keynote. He underscored the critical role of industry-academia collaboration in addressing real-world problems through innovative research and development. His talk provided practical perspectives from the industry, encouraging faculty to bridge the gap between theory and application.

**Dr. Shivakumar M.**, Principal, GSSSIETW, felicitated the Chief Guest and spoke on the importance of attending FDPs. He emphasized that such programs equip faculty members with the latest trends, technologies, and methodologies, ultimately leading to enhanced teaching effectiveness and improved classroom engagement. The FDP coordinators **Dr. Shymala.C.**, Associate Professor, **Dr. Anitha.K.**, Assistant Professor & IETE coordinator and faculties from various departments of the institute were present during the event.

The event concluded with a valedictory function, Mr. Yuvaraju, Assistant Professor, Dept of ECE has rendered vote of thanks for gracing the occasion and the participants were given with participation certificates.

Smt. Anupama B Pandit, Secretary, GSSS(R) and Sri Bharath R K, Member & CEO, GSSS(R) congratulated HoD, Teaching and non-teaching staff of ECE Department for successfully conducting the FDP event.

*Balaji*  
Coordinator

*R. Rajendra R. Patil*  
HoD, ECE 24/07/25  
Head of the Department  
Dept of Electronics & Communication Engg.  
GSSSIETW, Mysuru, India-570016

*Dr. Shivakumar M.*  
Principal 24/07/2025  
Principal  
GSSSIETW, Mysuru-16