



Department of Information Science and Engineering

Accredited by NBA, New Delhi
(Validity: 01.07.2017 - 30.06.2020)

To,
The Principal,
GSSSIETW,
Mysuru.

Date: 04/04/2019

Respected Sir,

Subject: *Requisition to conduct Two Day Workshop on "Microprocessors and its Applications" for IV Semester students from 04th to 05th April 2019 and release an amount of Rs. 5,750/- for the same.*

With reference to the above subject Department of Information Science Engineering intends to organize a Two Day Workshop on "**Microprocessors and its Applications**" for students of IV Semester. The Program will be conducted by Mr. Badrinath K, CEO, IORA Technologies, Bengaluru. The prerequisites for the workshop are basics of microprocessor. The Workshop includes hands on session for students to write machine-code and assembly language programming tasks which could be immediately executed. The workshop of "Microprocessor and its Application" is free of cost for students. The expected expenditure to organize the events is Rs. 5,750 [Rupees Five Thousand and Seven Hundred and Fifty only]. The details are indicated below. We request you to permit us to organize the workshop and kindly extend your support for the same, by approving above said amount from Special Budget.

Details:

Sl. No.	Particulars	Amount in Rs.
1	Remuneration for the Resource Person	2,000/-
2	Accommodation	1,300/-
3	Lunch, Tea, Biscuits(for 2 days)	300/-
4	Certificate print	650/-
5	Cash Prize for student	1,500/-
	Total Estimated Expenses(in Rupees)	5,750/-

G. Dath
Coordinator
4/4/19

Soumya Dath G

HOD

Dept. of ISE

R. Srinivasan
4/4/2019

Professor and Head
Department of Information Science and Engineering
GSSSIETW, K.R.S. ROAD, Mysuru-570 016.



Geetha Shishu Shikshana Sangha (R)

GSSS INSTITUTE OF ENGINEERING AND TECHNOLOGY FOR WOMEN

(Affiliated to VTU, Belagavi, Approved by AICTE, New Delhi & Govt. of Karnataka)

KRS Road, Metagalli, Mysuru 570 016, Karnataka, India

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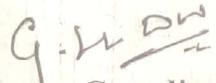
Date: 03/04/2019

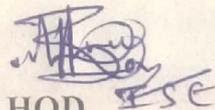
CIRCULAR

All the 4th Semester students are hereby informed to attend Tow day workshop arranged by Dept. of ISE under CSI on 4th and 5th April 2019 without fail. Attendance is compulsory.

Details are given bellow:

Timings	Sem	Event	Resource Person	Venue
09.00 AM to 05.00 PM	IV	Two day Workshop on "Essentials of Prototyping Engineering Product Using ARM7 - LPC2148"	Dr. Badari Nath K., Chief Technical Consultant, IORA Technologies, Bengaluru	A-301


Event Coordinator
[Soumya Dath G]


HOD ISE
Professor and Head
Department of Information Science and Engineering
GSSSIETW, K.R.S. ROAD, Mysuru-570 016.



Geetha Shishu Shikshana Sangha (R)

GSSS Institute of Engineering & Technology for Women

(Affiliated to VTU, Belagavi, Approved by AICTE, New Delhi & Govt. of Karnataka)

MYSURU-570 016 | Karnataka | India

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Organizes.

Two Day Workshop on

“ESSENTIALS OF PROTOTYPING ENGINEERING PRODUCT USING ARM7 - LPC2148”

Under IQAC

In Association with

Computer Society of India

CERTIFICATE



Increase Space

This is to certify that.....

.....
.....
participated in Two Day Workshop on “Essentials of Prototyping Engineering Product Using ARM7- LPC2148” Organized by Department of Information Science & Engineering, GSSSIETW, Mysuru held on April 4th & 5th 2019.

Dr. Badri Nath K
Chief Technical Consultant
IORA Technologies
Bangalore

Dr. Reshma Banu
Professor & Head,
Dept. of ISE.

Dr. Shivakumar M
Dr. Shivakumar M
Principal,
GSSSIETW, Mysuru.

Department of Information Science and Engineering

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Date: 05/04/2019

FEEDBACK FORM

Event: Two day Workshop on "Essentials of Prototyping Engineering Product Using ARM7 - LPC2148

Resource Person: Dr. Badari Nath K., Chief Technical Consultant, IORA Technologies, Bengaluru

Semester :- IV

Does this event:

- Q1 : Provide Knowledge of modern engineering and IT tools for complex engineering activities with its limitation.
- Q2 : Motivation for independent and life-long learning with technological changes.
- Q3 : Design/develop solutions for complex engineering problems.

Ratings: Excellent-3

Good-2

Satisfactory-1

Poor-0

Sl. No	USN	Student Name	Q1	Q2	Q3	Please provide remarks for rating less than 2 to improve the event quality	Signature with Date
1	4GW17IS001	ANUSHA C SHEKAR	3	3	3		Anusha C Shekar
2	4GW17IS002	BHAVANA N	3	3	3		Bhavana N
3	4GW17IS003	BHUVANA A N	3	3	3		Bhuvana A N
4	4GW17IS004	CHAITHRA K	3	3	3		Chaitra K
5	4GW17IS005	CHINMAYI S	3	3	3		Chinmayi S
6	4GW17IS006	DARSHITHA K S	3	3	3		Darshitha K S
7	4GW17IS007	GANASHREE G	3	3	3		G. Ganashree
8	4GW17IS008	GNANESHWARI D S	3	3	3		Gnaneshwari D S
9	4GW17IS009	HARINI C B	3	3	3		Harini C B
10	4GW17IS010	HARSHITHA C	3	3	3		Harshitha C
11	4GW17IS011	HARSHITHA N	3	3	3		Harshitha N
12	4GW17IS012	HARSHITHA V	3	3	3		Harshitha V
13	4GW17IS013	HOOR AFZA KHANUM	3	3	3		Hoor Afza
14	4GW17IS014	KAVYA N S	3	3	3		Kavya N S
15	4GW17IS015	KUSUMITHA N	3	3	3		Kusumitha N
16	4GW17IS016	LIKITHA M P	3	3	3		Likitha M P
17	4GW17IS017	MADHUSHREE R	3	3	3		Madhushree R
18	4GW17IS018	MAHIMA R				AB	
19	4GW17IS019	MEDHA R	3	3	3		Medha R

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Sl. No	USN	Student Name	Q1	Q2	Q3	Please provide remarks for rating less than 2 to improve the event quality	Signature with Date
20	4GW17IS020	NAMANA PRASANNA	3	3	3		Namany
21	4GW17IS021	NEHA S P	3	3	3		Neha
22	4GW17IS022	PRANATHY G S	3	2	3		Pranathy
23	4GW17IS023	R SHRIYA	3	2	3		R Shriya
24	4GW17IS024	RANJITHA S K	3	3	3		Ranjitha
25	4GW17IS025	RASHIKA S	3	3	2		Rashika
26	4GW17IS026	SANJANA K J	3	3	3		Sanjana
27	4GW17IS027	SHREYA M G	3	3	3		Shreyamg
28	4GW17IS028	SONA GANESH G	3	3	3		Sona
29	4GW17IS029	SPANDANA M	3	3	3		Spandana M 08/4/19
30	4GW17IS030	SPOORTHI M	3	3	3		Spoorti M 8/4/19
31	4GW17IS031	SUSHMA N	3	3	3		Sushma N 8/4/19
32	4GW17IS032	SUSHMASHREE B S	3	3	3		Sushma 8/4/19
33	4GW17IS033	SYEDA SUKAINA FATIMA	3	3	3		Syedat Tamiyat
34	4GW17IS034	TASMIA KHAN	3	3	3		Tasmia
35	4GW17IS035	THANUSHREE G N	3	3	3		Thanushree
36	4GW17IS036	THEJASWINI K M	3	3	3		Thejaswini
37	4GW17IS037	THUSHARA N	3	3	3		Thushara
38	4GW17IS038	VINUTHA M	3	3	3		Vinutha M 8/4/19
39	4GW17IS039	YASHASWINI P	3	2	3		Yashaswini P
40	4GW17IS040	YASHASWINI S	3	3	3		Yashaswini S 8/4/19
41	4GW16IS050	SOWJANYA	3	3	2		Sowjanya 8/4/19
Average			3	3	2	Observation	Coordinator Signature with Date
Attainment Level						Students were able to gain knowledge on micro controllers & embedded system with extensive applications of ARM processors	

G. Dath G
EVENT COORDINATOR
Soumya Dath G

Chaya P
NBA COORDINATOR
Chaya P

Dr. Reshma Banu
HOD
Dr. Reshma Banu
Professor and Head
Department of Information Science and Engineering
GSSSIETW, K.R.S. ROAD, Mysuru-570 016.



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**Press Report on
Two day Workshop on “Essentials of Prototyping Engineering Product Using ARM7
- LPC2148”
Held recently (April 2019)
For IV Semester students**

Department of Information Science & Engineering had organized a Two Day Workshop on “Essentials of Prototyping Engineering Product Using ARM7 - LPC2148” in association with Computer Society of India, Bangalore and Mysore Chapter, under IQAC.

The program began with inaugural function with Resource Person **Dr. Badari Nath K.**, Chief Technical Consultant, IORA Technologies, Bengaluru, and **Dr. Reshma Banu**, HOD, Department of Information Science & Engineering, GSSSIETW, Mysuru gracing the occasion. Kum. Chinmayi S of 4th sem, Information Science & Engineering welcomed the gathering and introduced the Resource Person, Dr. Badari Nath K, Chief Technical Consultant, IORA Technologies, Bengaluru.



Resource Person Dr. Badari Nath K, Chief Technical Consultant, IORA Technologies, Bengaluru, Dr. Reshma Banu, Professor & HOD, Dept. of ISE, Staff and VI Semester students of Department of Information Science & Engineering, GSSSIETW, Mysuru

Objectives of the workshop:

This workshop enable students to

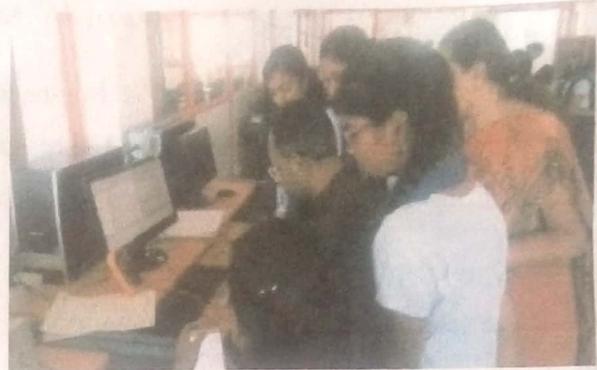
- Understand the architecture of LPC2148 microcontroller.
- Gain Knowledge to develop projects using ARM7/LPC2148.
- Promote individual and team co-ordination with learning experience.

Day 1 (4th April 2019)

The session started with the basics of Microprocessor, Microcontrollers and Embedded Systems and discussed the features of the same. The resource person briefed about basics of ARM processors which are extensively used in consumer electronic devices such as smart phones, tablets, multimedia players. The ARM processors' smaller size, reduced complexity and lower power consumption makes them suitable ^{for} increasingly miniaturized devices.



Dr. Badari Nath K, Chief Technical Consultant, IORA Technologies, Bengaluru, discussing with staff and students of Department of Information Science & Engineering, GSSSIETW, Mysuru



Dr. Badari Nath K, Chief Technical Consultant, IORA Technologies, Bengaluru, explaining the working of Stepper motor with ARM 7/LPC2148 to staff & students.

Day 2 (5th April 2019)

The session started about assembler / cross assembler and Integrated Development Environment (IDE) kit i.e., ^{Keil} ~~Keil~~ and Flash magic tools to start with programming. Further students were divided into 10 groups to carry out the experiments. The afternoon session continued about explaining the basics of the experiment to the individual batch to interface Stepper motor with ARM7/LPC2148. Students were made to implement the program on the rotation of stepper motor ^{which} ~~and it~~ was demonstrated. The session concluded with completing the stepper motor experiment successfully.

^{During the workshop,}
~~All the sessions of the two day workshop had~~ assignments ^{which} ~~for~~ students on the topics covered during the sessions. The Workshop concluded with the Valedictory function which was attended by all the students and faculties of the Information Science & Engineering department where the cash prize was awarded for the best three projects.



Valedictory function: Dr. Badari Nath K, Chief Technical Consultant, IORA Technologies, Bengaluru and Dr. Reshma Banu, Professor and Head, Dept. of ISE distributing the cash prize for the best project team.

The workshop was much appreciated by the students with many of the enthusiastic participants sharing their experience on how the workshop enlightened them about practical knowledge of working on ARM7/ LPC2148 and motivated them to do more projects using ARM7/ LPC2148.

OUTCOMES:

- Students understood the architecture of LPC2148 microcontroller.
- Executed small assignment programs using ARM7/LPC2148.
- Workshop boosted student's confidence in Microprocessor subject.
- Promoted team coordination.

G. L. Dath

Event Coordinator
Soumya Dath G

[Signature]
HOD

Professor and Head
Dept. of Information Science & Engineering
GSSSIETW, KRS Road, Mysuru - 570 016

el. Selivalcumag
PRINCIPAL

<https://citytoday.news/two-day-technical-workshop-held-at-gsssietw/>

5/3/2019

Two-day technical workshop held at GSSSIETW - Mysuru Today

citytoday



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Headlines

Two-day technical workshop held at GSSSIETW

Murali © 11/04/2019

Mysuru, April 11:- Department of Information Science and Engineering, GSSSIETW, organised a two-day workshop on “Essentials of prototyping engineering product using ARM7 – LPC2148” in association with Computer Society of India, Bengaluru and Mysuru chapter under IQAC.

The programme began with inaugural function with resource person Dr Badari Nath K, chief technical consultant, IORA Technologies, Bengaluru, and Dr Reshma Banu, HoD, Department of Information

city today e-paper dated
11th April 2019.

Science and Engineering, GSSSIETW, Mysuru, gracing the occasion.

Objectives of the workshop: This workshop has enabled students to understand the architecture of LPC2148 microcontroller. Gain knowledge to develop projects using ARM7/LPC2148. Promote individual and team coordination with the learning experience.

On day 1 the session started with the basics of microprocessor, microcontrollers and embedded systems and discussed the features of the same. The resource person briefed about basics of ARM processors which are extensively used in consumer electronic devices such as smartphones, tablets, multimedia players. The ARM processor's smaller size, reduced complexity and lower power consumption make them suitable for miniaturised devices.

On day 2 the session discussed assembler/cross assembler and Integrated Development Environment (IDE) kit i.e., Keil and Flash magic tools to start with programming. Further students were divided into 10 groups to carry out the experiments. The afternoon session continued about explaining the basics of the experiment to the individual batch to an interface stepper motor with ARM7/LPC2148. Students were made to implement the programme on the rotation of stepper motor which was demonstrated. The session concluded with completing the stepper motor experiment successfully.

During the workshop, assignments were given to the students on the topics covered during the sessions. The Workshop concluded with the valedictory function which was attended by all the students and faculties of the Information Science and Engineering Department where the cash prize was awarded for the best three projects.

(MR)
